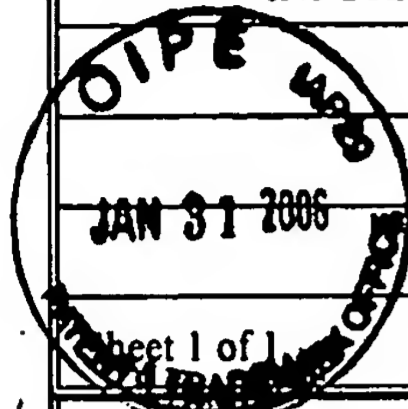


PTO/SB/08B (Substitute for form 1449/PTO)		Attorney Docket No.: 004-8428
		Application No.: 10/621,072
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		First Named Inventor: Mark S. Moir
		Filing Date: July 16, 2003
		Group Art Unit: 2189
		Examiner Name: Horace L. Flournoy
		Date Submitted: January 27, 2006



### NON PATENT LITERATURE DOCUMENTS

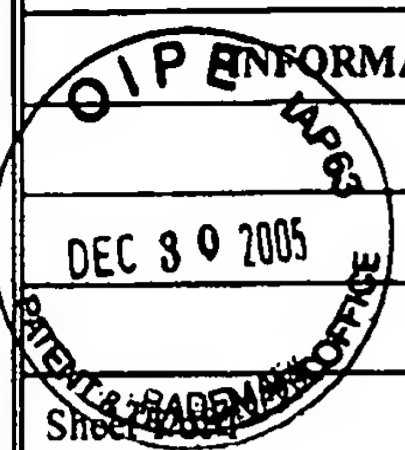
Examiner Initials*	Cite No. <sup>1</sup>	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
AF	1	AFEK, Yehuda, "Atomic Snapshots of Shared Memory," Journal of the ACM, vol. 40, no. 4, pp. 873-890, 1993.	
AF	2	AGESEN, Ole et al., "An Efficient Meta-Lock for Implementing Ubiquitous Synchronization," ACM SIGPLAN Notices, vol. 34, no. 10, pp. 207-222, October 1999.	
AF	3	ANDERSON, James H. et al., "Universal Constructions for Large Objects," IEEE Transactions on Parallel and Distributed Systems, vol. 10, no. 12, pp. 1317-1332, 1999.	
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Examiner Signature		Date Considered	06/05/06
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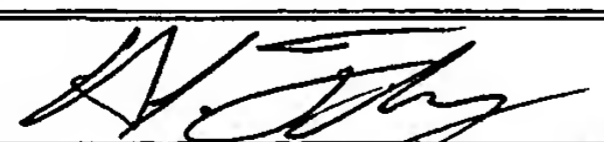
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	First Named Inventor: Mark S. Moir
	Filing Date: July 16, 2003
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	Examiner Name: Horace L. Flournoy
	Date Submitted: December 28, 2005

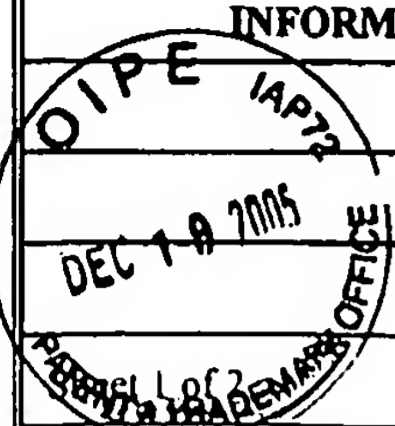
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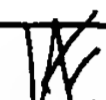



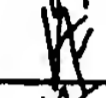
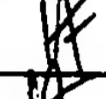
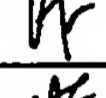
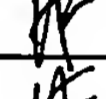
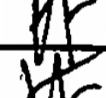
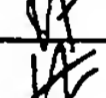
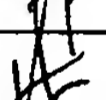




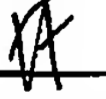

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AF	1	HERLIHY, M.P., "Wait-Free Synchronization," <i>ACM Transactions On Programming Languages and Systems</i> , 11(1):124-149, January 1991.	
AF	2	MASSALIN, H., et al., "A Lock-Free Multiprocessor OS Kernel," Technical Report TR CUCS-005-9, Columbia University, New York, NY, 1991, 21 pages.	
AF	3	MASSALIN, Henry, "Synthesis: An Efficient Implementation of Fundamental Operating System Services," Dissertation submitted in partial fulfillment of the requirements for the Degree of Doctor of Philosophy in the Graduate School of Arts and Sciences, Columbia University, New York, NY, online, 158 pages, 1992 [retrieved from the Internet on 2001-07-13: URL:ftp://ftp.cs.columbia.edu/reports/reports-1992/cucs-039-92.ps.gz].	
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AF	10	ATTIYA, Hagit, et al., "Atomic Snapshots In O(n log n) Operations," <i>SIAM Journal on Computing</i> , 27(2):319-340, April 1998.	
Examiner Signature			Date Considered 06/05/06

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PTO/SB/08A (Substitute for form 1449/PTO)	Attorney Docket No.: 004-8428
	Application No.: 10/621,072
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> 	First Named Inventor: Mark S. Moir
	Filing Date: July 16, 2003
	Group Art Unit: 2189
	Examiner Name: Horace L. Flournoy
	Date Submitted: December 15, 2005

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		US-4,584,640	04-22-1986	MacGregor et al.		
		US-4,847,754	07-11-1989	Obermarck et al.		
		US-5,224,215	06-29-1993	Disbrow, John R.		
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		US-6,144,965	11-07-2000	Olivier, Richard J.		
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		US-6,360,219	03-19-2002	Bretl et al.		
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		US-6,366,932	04-02-2002	Christenson, David Alan		
		US-6,581,063	06-17-2003	Kirkman, Richard Karl		
		US-6,651,146	11-18-2003	Srinivas et al.		
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		EP 0 366 585	05-02-1990	IBM		
		EP 0 466 339	01-15-1992	IBM		
		WO 86/00434	01-16-1986	Motorola		
		WO 01/53942	07-26-2001	Steele, Jr. et al.		
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<sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04.

<sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3)

<sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

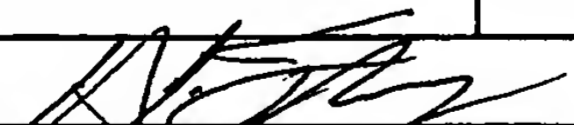
<sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.

<sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT		First Named Inventor: Mark S. Moir
(Continuation Sheet)		Filing Date: July 16, 2003
		Group Art Unit: 2189
		Examiner Name: Horace L. Flournoy
Sheet 2 of 2		Date Submitted: December 15, 2005

U.S. PATENT DOCUMENTS					
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VF		US-6,826,757	11-30-2004	Steele, Jr. et al.	
VF		US-2001/0047361	11-29-2001	Martin et al.	
VF		US-2003/0140085	07-24-2003	Moir et al.	
VF		US-2003/0174572	09-18-2003	Moir et al.	
VF		US-2003/0182462	09-25-2003	Moir et al.	
VF		US-2003/0182465	09-25-2003	Moir et al.	
VF		US-2004/0153687	08-05-2004	Moir et al.	


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VF		WO 01/53943	07-26-2001	Steele, Jr. et al.		
VF		WO 01/80015	10-25-2001	Martin et al.		
VF		WO 01/82057	11-01-2001	Moir et al.		
VF		WO 03/060705	07-24-2003	Moir et al.		
		WO 03/060715	07-24-2003	Moir et al.		
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**LIST OF PENDING U. S. PATENT APPLICATIONS**

The following pending U.S. patent applications are listed for consideration by the Examiner.

Examiner Initials	Cite No.	Document Identification
AF	1	US Patent Application No. 09/547,288 (Attorney Docket No. 004-4663), filed April 11, 2000 and naming as inventor(s) Shavit et al.
AF	2	US Patent Application No. 09/547,290 (Attorney Docket No. 004-4664), filed April 11, 2000 and naming as inventor(s) Shavit et al.
AF	3	US Patent Application No. 09/710,218 (Attorney Docket No. 004-4896), filed November 10, 2000 and naming as inventor(s) Harris, Timothy
AF	4	US Patent Application No. 09/837,671 (Attorney Docket No. 004-5723), filed April 18, 2001 and naming as inventor(s) Detlefs et al.
Examiner Signature: 		
Date Considered:		06/05/06

***Applicant(s) are providing a separate list of pending applications, pursuant to MPEP § 609III(D), to avoid publication of the application numbers on any patent that issues from the above-identified application. Applicant(s) request that the Examiner indicate consideration of the pending applications listed above in accordance with MPEP 609III(C)(2) and return a copy of this page with the next communication.***

U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.: 004-8428
		Application No.: 10/621,072
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		Applicant(s): Moir et al.
		Filing Date: July 16, 2003
		Group Art Unit: 2188
		Date Submitted: September 27, 2004
NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
AF	1	Afek, Yehuda et al., "Long-Lived Renaming Made Adaptive", 18 <sup>th</sup> Annual ACM Symposium on Principles of Distributed Computing, pages 91-104, 1999.
AF	2	Afek, Yehuda, "Wait-Free Made Fast", 27 <sup>th</sup> Annual ACM Symposium on Theory of Computing, pages 538-547, 1995.
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AF	17	Herlihy, Maurice et al., "Transactional Memory: Architectural Support for Lock-Free Data Structures", 20 <sup>th</sup> International Symposium in Computer Architecture, 1993.
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				Group Art Unit: 2188		
				Date Submitted: June 17, 2004		
<b>U.S. Patent Documents</b>						
*Examiner Initial		Document Number	Date	Name		
VF	1	5,428,761	Jun. 27, 1995	Herlihy et al.		
VF	2	US2004/0015510 A1	Jan. 22, 2004	Moir et al.		
VF	3	US2004/0034673 A1	Feb. 19, 2004	Moir et al.		
<b>Foreign Patent Documents</b>						
					<b>Translation</b>	
		Document	Date	Country	Yes	No
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
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